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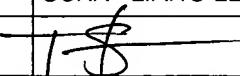
Total Number of Pages in This Submission

Application Number	10/779,904-Conf. #9608
Filing Date	February 17, 2004
First Named Inventor	Masahiro Ishida
Art Unit	2133
Examiner Name	Not Yet Assigned
Attorney Docket Number	02008/071003

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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	Remarks	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	OSHA · LIANG LLP		
Signature			
Printed name	Thomas K. Scherer		
Date	June 29, 2006	Reg. No.	45,079



Docket No.: 02008/071003
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Masahiro Ishida et al.

22511
PATENT TRADEMARK OFFICE

Application No.: 10/779,904

Confirmation No.: 9608

Filed: February 17, 2004

Art Unit: 2133

For: METHOD AND APPARATUS FOR DEFECT
ANALYSIS OF SEMICONDUCTOR
INTEGRATED CIRCUIT

Examiner: Not Yet Assigned

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 CFR 1.97, Applicant(s) hereby make of record the following additional documents. A PTO Form SB/08 and a full copy of each of the documents required under 37 CFR 1.98(a)(2) accompany this statement.

Applicant(s) have become aware of the following documents, cited in a Japanese Office Action issued April 4, 2006, during the prosecution of application no. JP 2000-101867, which corresponds to the above referenced application, and in accordance with 37 CFR 1.97(c) and (e)(1) or (b)(3), hereby submit(s) these documents for the Examiner's consideration. These documents are cited on the enclosed PTO Form SB/08, and a copy of the Japanese Office Action and of each document required under 37 CFR 1.98(a)(2) cited thereon are enclosed as well.

This statement is not to be interpreted as a representation that the cited documents are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any document herein be construed *per se* as a representation that such document is prior art. Moreover, Applicant(s) understand(s) the Examiner will make an independent evaluation of the cited documents.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0591, under Order No. 02008/071003.

Dated: June 29, 2006

Respectfully submitted,

By 
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Application No. (if known): 10/779,904

Attorney Docket No.: 02008/071003

Certificate of Express Mailing Under 37 CFR 1.10

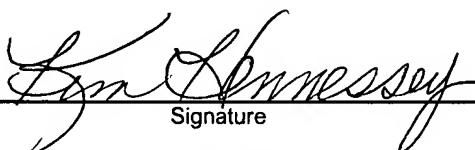
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Transmittal (1 page)
Supplemental Information Disclosure Statement (2 pages)
IDS (Citation) by Applicant (24 References) (2 pages)
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Substitute for form 1449A/B/PTO

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	10/779,904-Conf. #9608
				Filing Date	February 17, 2004
				First Named Inventor	Masahiro Ishida
				Art Unit	2133
				Examiner Name	Not Yet Assigned
Sheet	1	of	2	Attorney Docket Number	02008/071003

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
BA	JP-4-244975		09-01-1992	Matsushita Electron Corp		
BB	JP-6-258388		09-16-1994	Hitachi Ltd		
BC	JP-8-201486		08-09-1996	Nec Corp		
BD	JP-9-26981		01-28-1997	Fujitsu Ltd		
BE	JP-9-54140		02-25-1997	Sharp Corp		
BF	JP-9-80114		03-28-1997	Advantest Corp		
BG	JP-10-125794		05-15-1998	NEC Corp		
BH	JP-10-239394		09-11-1998	NEC Corp		
BI	JP-11-174114		07-02-1999	Toshiba Corp		
BJ	JP-60-165562		08-28-1985	Vladimirsky et al.		
BK	JP-62-39780		02-20-1987	Mitsubishi Electric Corporation		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CA	Patent Abstracts of Japan; Publication number: 04-244975; Date of Publication: 09/01/1992 (2 pages)		
CB	Patent Abstracts of Japan; Publication number: 06-258388; Date of Publication: 09/16/1994 (2 pages)		
CC	Patent Abstracts of Japan; Publication number: 08-201486; Date of Publication: 08/09/1996 (2 pages)		
CD	Patent Abstracts of Japan; Publication number: 09-026981; Date of Publication: 01/28/1997 (2 pages)		
CE	Patent Abstracts of Japan; Publication number: 09-054140; Date of Publication: 02/25/1997 (2 pages)		
CF	Patent Abstracts of Japan; Publication number: 09-080114; Date of Publication: 03/28/1997 (2 pages)		
CG	Patent Abstracts of Japan; Publication number: 10-125794; Date of Publication: 05/15/1998 (2 pages)		
CH	Patent Abstracts of Japan; Publication number: 10-239394; Date of Publication: 09/11/1998 (2 pages)		
CI	Patent Abstracts of Japan; Publication number: 11-174114; Date of Publication: 07/02/1999 (2 pages)		
Examiner Signature		Date Considered	



PTO/SB/08a/b (07-05)

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				Art Unit	2133
				Examiner Name	Not Yet Assigned
Sheet	2	of	2	Attorney Docket Number	02008/071003

		(pages)	
	CJ	Patent Abstracts of Japan; Publication number: 11-174114; Date of Publication: 07/02/1999 (2 pages)	
	CK	"Transient Power Supply Current Testing of Digital CMOS Circuits"; Rafic Z. Makki et al.; International Test Conference; Paper 38.1; IEEE; 1995; pp. 892-901	
	CL	"Defect Detection with Transient Current Testing and its Potential for Deep Sub-micron CMOS ICs"; Manoj Sachdev et al; International Test Conference; IEEE 1998; Paper 8.4; pp. 204-213	
	CM	Translation of Japanese Office Action dated April 4, 2006 (2 pages)	

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Examiner Signature	Date Considered
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